

FIG. 1

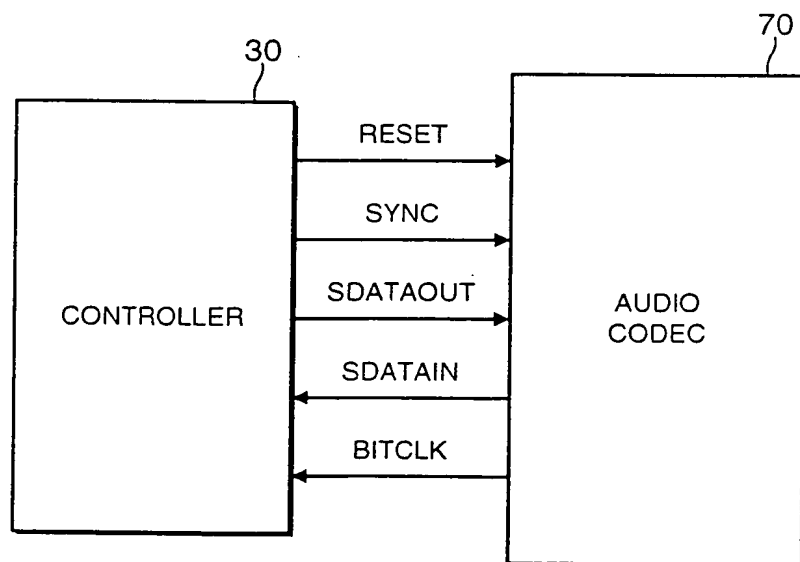


FIG. 2

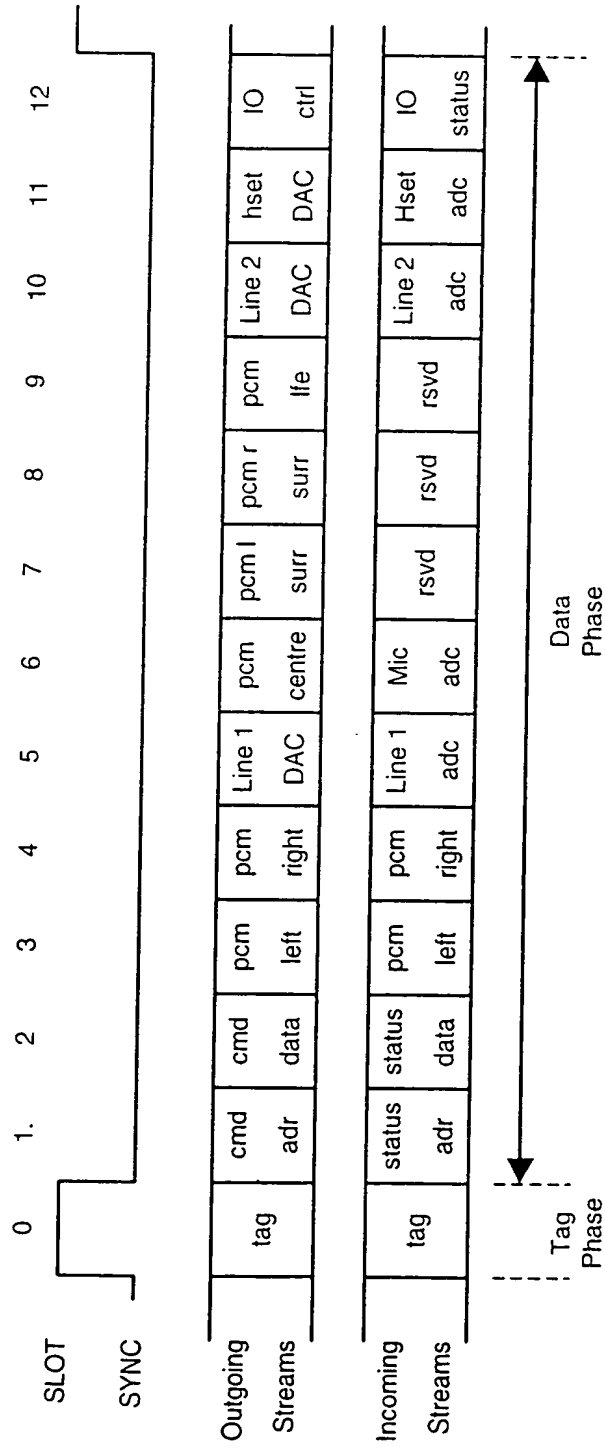


FIG. 3

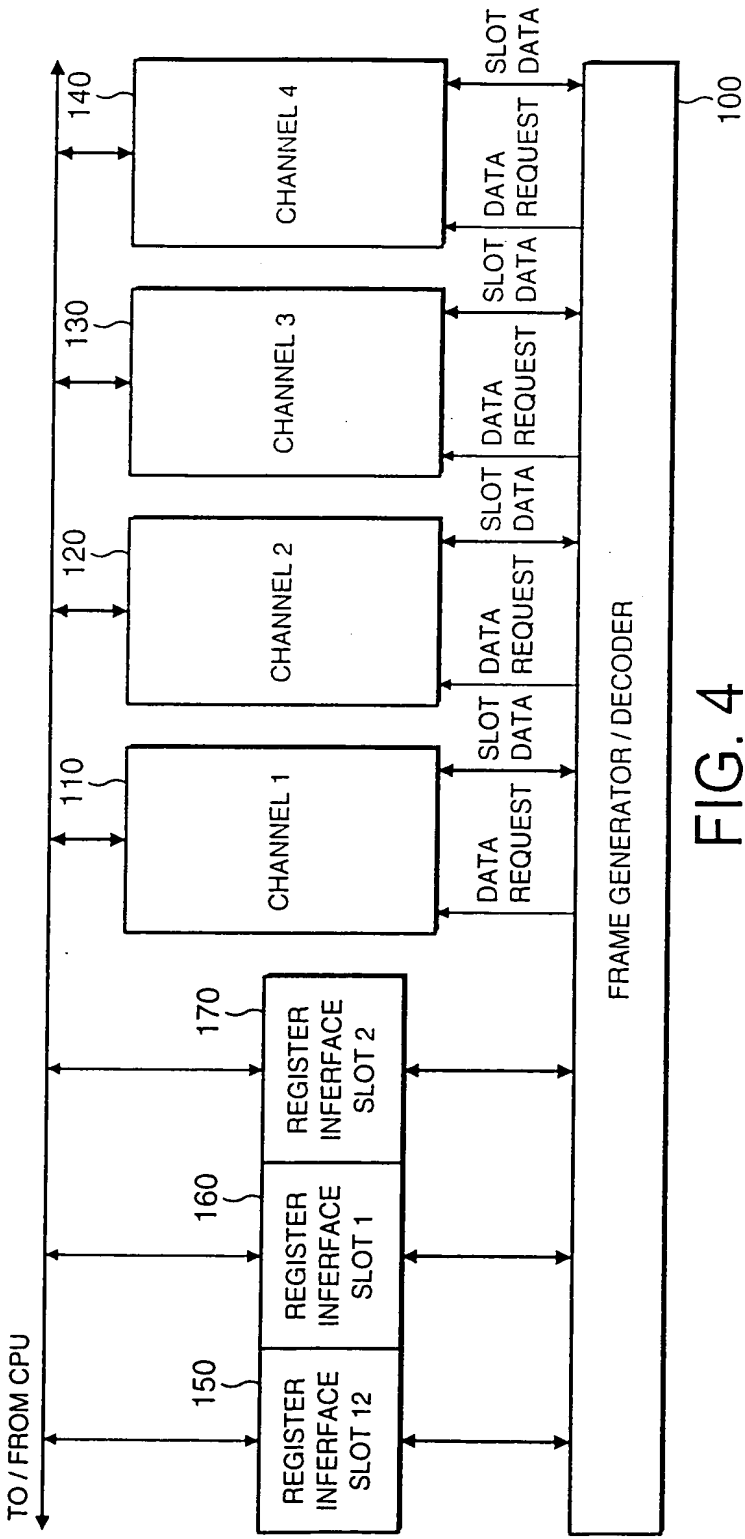


FIG. 4

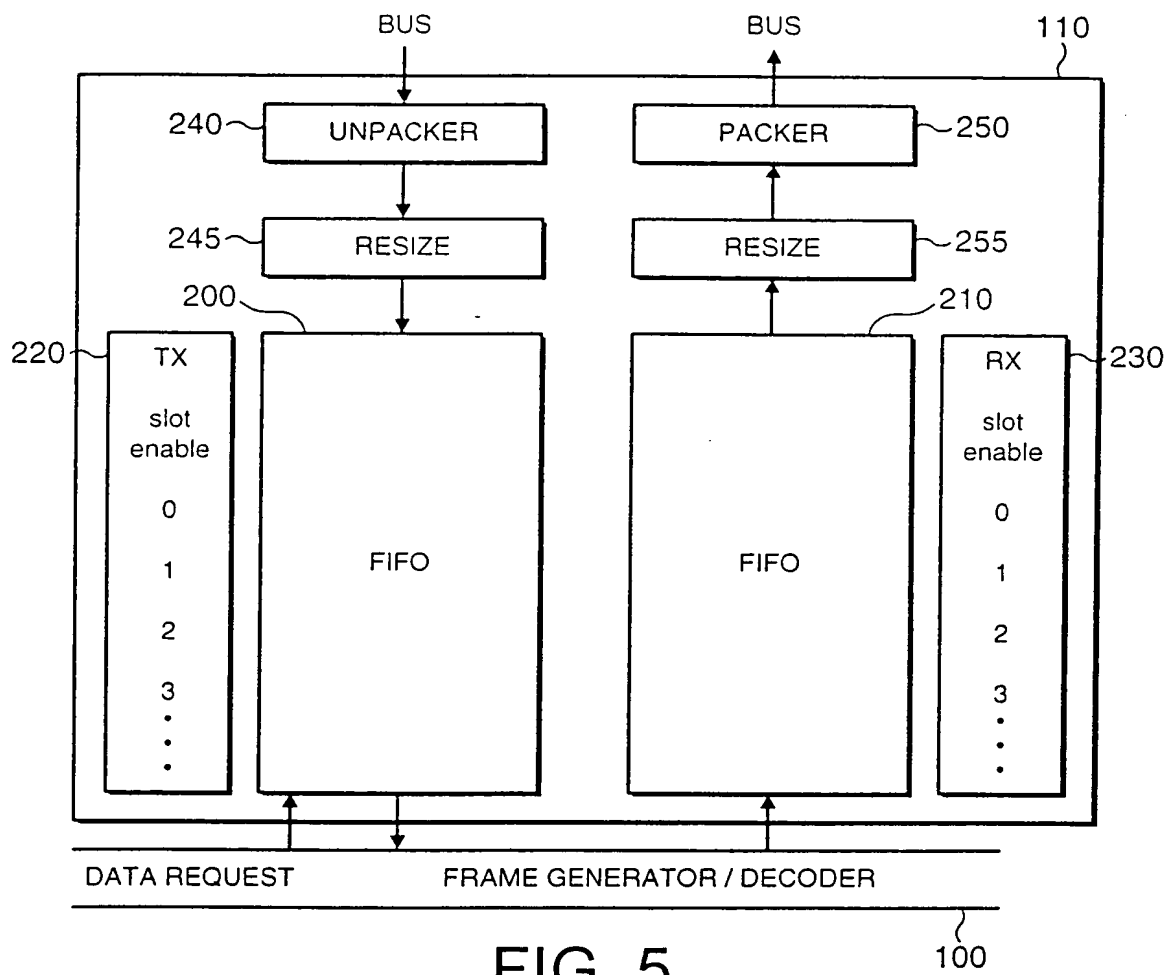


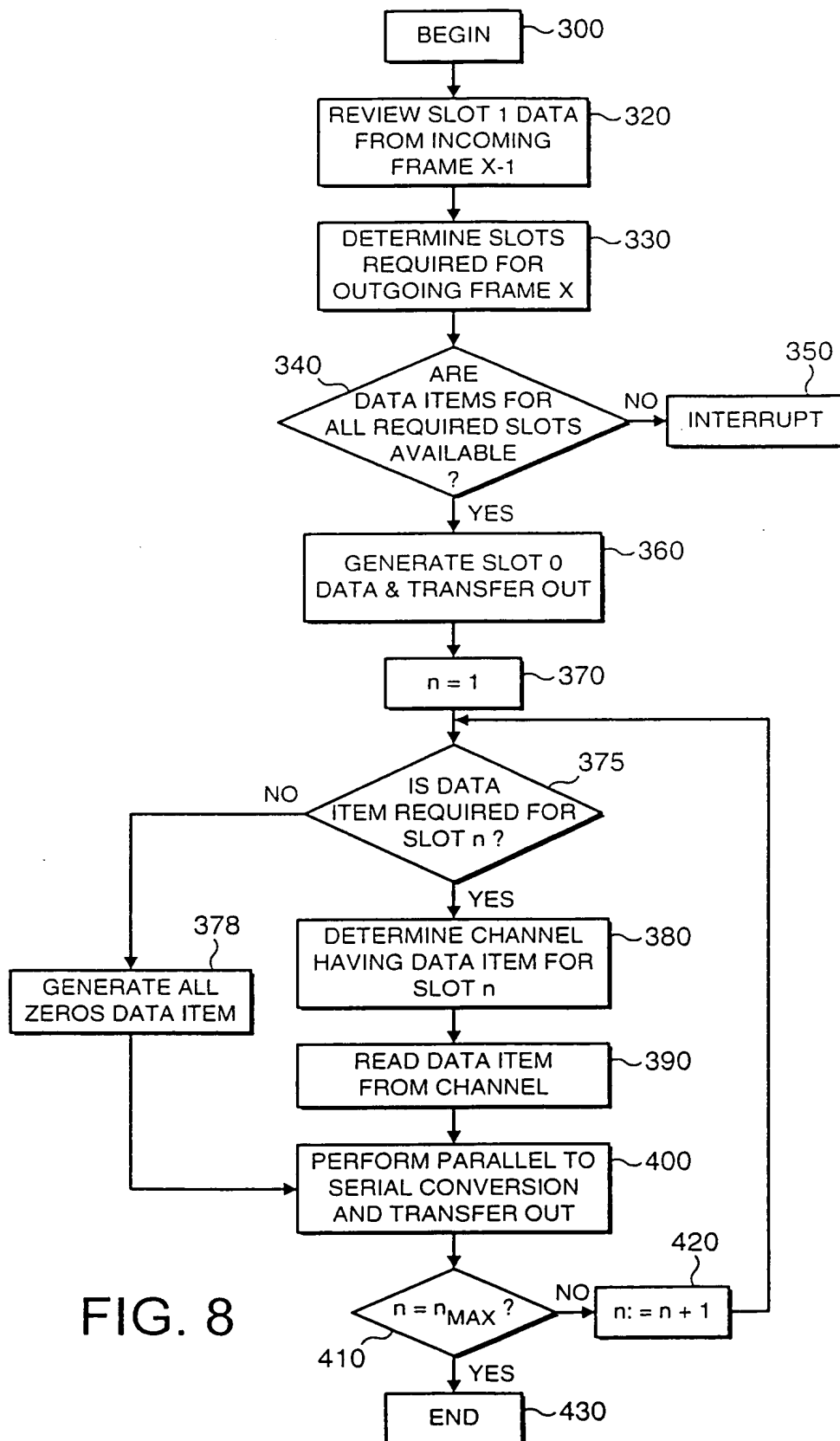
FIG. 5

Bits	Name	Function
15	CM	Compact mode enable. If this bit is set to 1 and the RSIZE value is either 12 or 16 the two data words will be compacted into a 32-bit word for reading by the CPU. If set to 0 a 32-bit word will contain one slot data.
14:13	RSIZE	00 – data is 16 bits 01 – data is 18 bits 10 – data is 20 bits 11 – data is 12 bits
12	RX12	FIFO stores SLOT12 data (takes precedence over individual SLOT 12 register)
11	RX11	FIFO stores SLOT11 data
10	RX10	FIFO stores SLOT10 data
9	RX9	FIFO stores SLOT9 data
8	RX8	FIFO stores SLOT8 data
7	RX7	FIFO stores SLOT7 data
6	RX6	FIFO stores SLOT6 data
5	RX5	FIFO stores SLOT5 data
4	RX4	FIFO stores SLOT4 data
3	RX3	FIFO stores SLOT3 data
2	RX2	FIFO stores SLOT2 data (only use if sampling rate is 48KHz)
1	RX1	FIFO stores SLOT1 data (only use if sampling rate is 48KHz)

FIG. 6

Bits	Name	Type	Function
15	CM	Read/write	Compact mode enable. If this bit is set to 1 and the TSIZE value is either 12 or 16 the two data words will be compacted into a 32-bit word for reading by the CPU. If set to 0 a 32-bit word will contain one slot data.
14:13	TSIZE	Read/write	00 – data is 16 bits 01 – data is 18 bits 10 – data is 20 bits 11 – data is 12 bits
12	TX12	Read/write	FIFO contains SLOT12 data (takes precedence over SLOT12RXTX)
11	TX11	Read/write	FIFO contains SLOT11 data
10	TX10	Read/write	FIFO contains SLOT10 data
9	TX9	Read/write	FIFO contains SLOT9 data
8	TX8	Read/write	FIFO contains SLOT8 data
7	TX7	Read/write	FIFO contains SLOT7 data
6	TX6	Read/write	FIFO contains SLOT6 data
5	TX5	Read/write	FIFO contains SLOT5 data
4	TX4	Read/write	FIFO contains SLOT4 data
3	TX3	Read/write	FIFO contains SLOT3 data
2	TX2	Read/write	FIFO contains SLOT2 data (only use if sampling rate is 48KHz)
1	TX1	Read/write	FIFO contains SLOT1 data (only use if sampling rate is 48KHz)

FIG. 7



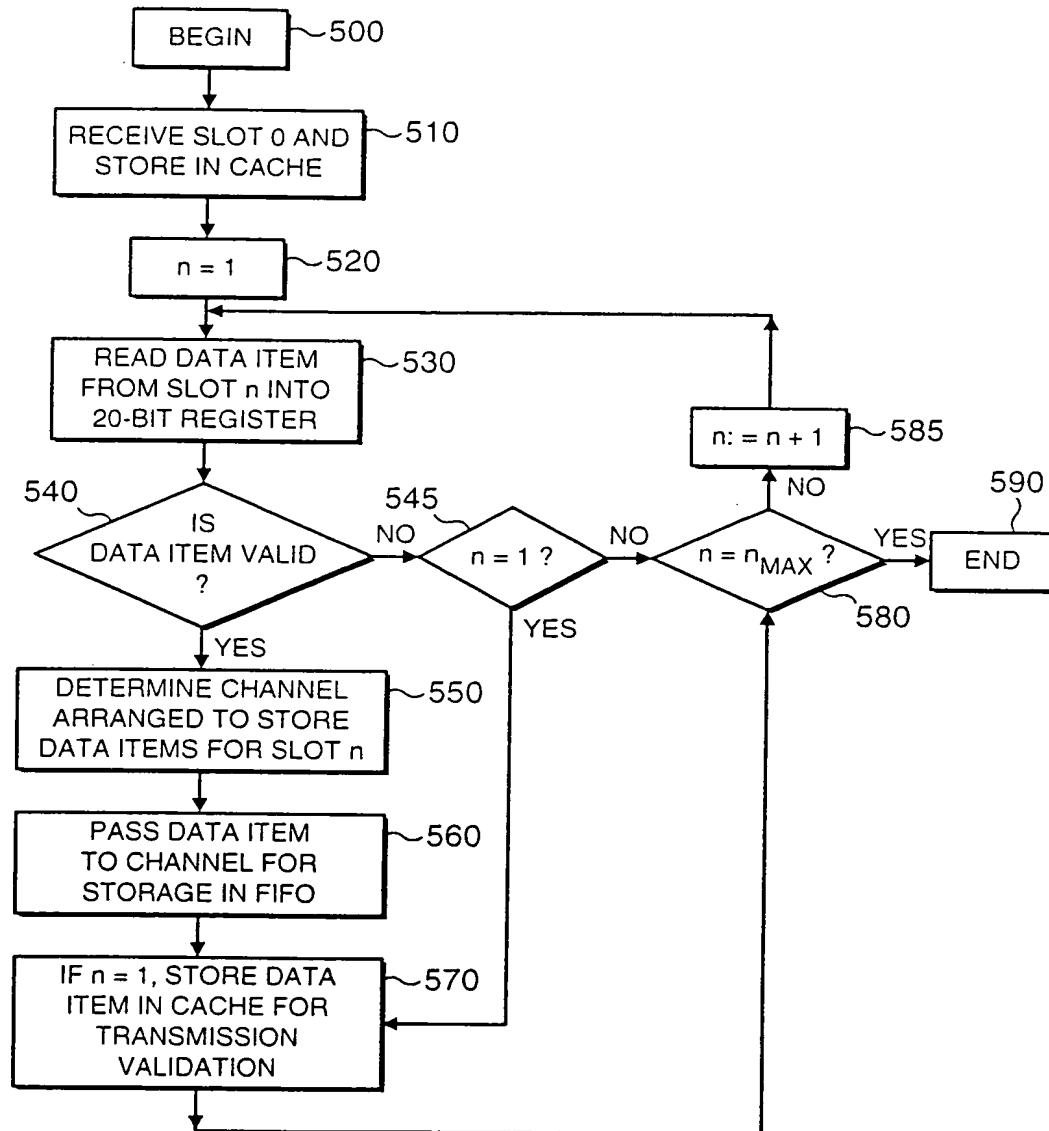


FIG. 9

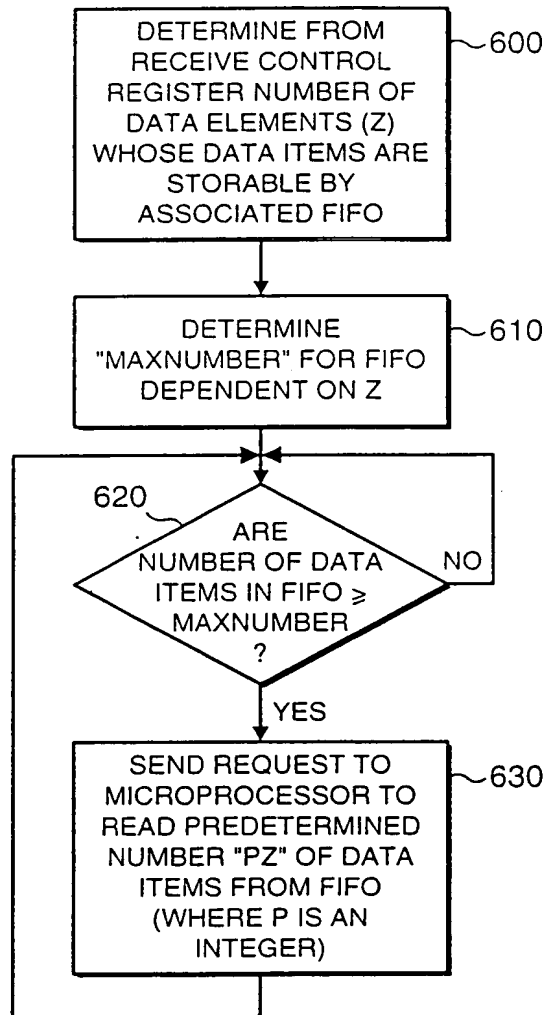


FIG. 10

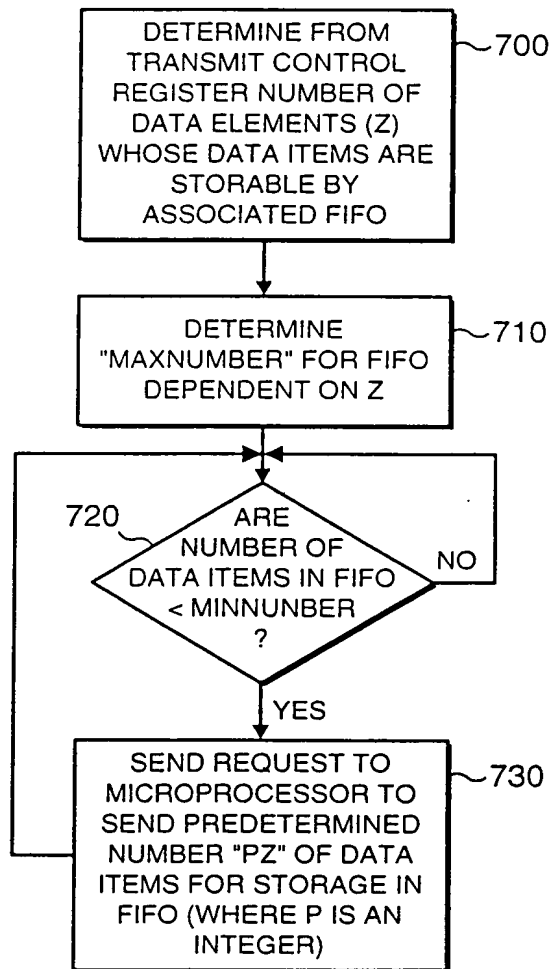


FIG. 11